100 A CONVENTIONAL METHOD OF
CALCULATING AGED DELAY TIME
OF A LOGIC LEVEL CIRCUIT

FIG.1
PRIOR ART
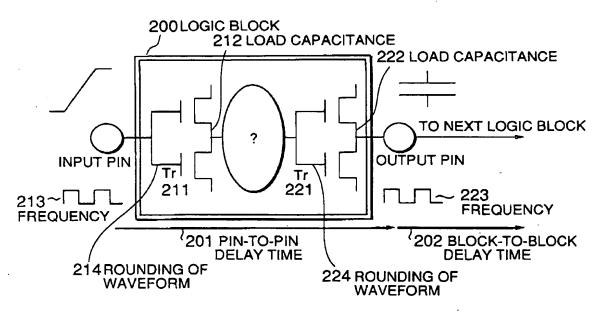

FIG.2

FIG.3

FIG.4
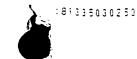
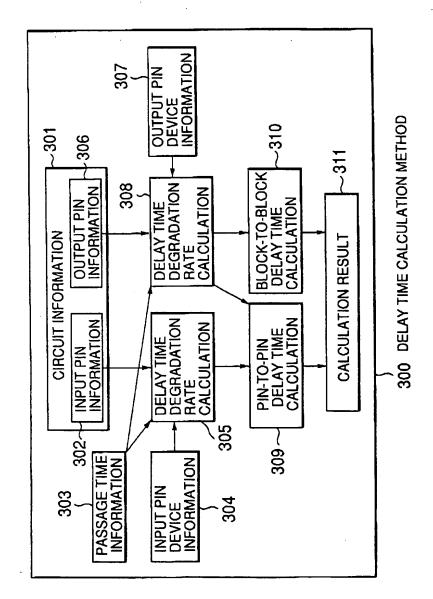400 DELAY TIME CALCULATION METHOD

VIN VOUT

CLOAD

Kn

VIN

tpd

VOUT

FIG.5
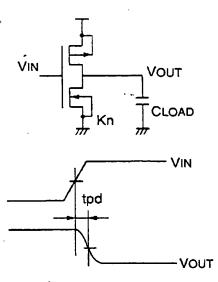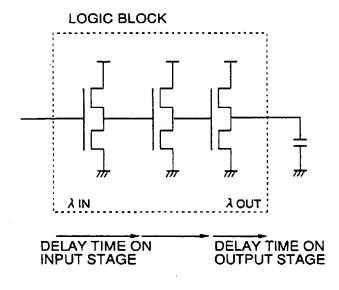
LOGIC BLOCK

λ IN   λ OUT

DELAY TIME ON INPUT STAGE → DELAY TIME ON OUTPUT STAGE

DELAY TIME OF A LOGIC BLOCK

FIG.6